

EAST Search History

EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S109	1	("20070228494").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2010/06/22 16:00
S110	0	("2007/0228494").URPN.	USPAT	OR	ON	2010/06/22 16:02
S111	2	(("6952027") or ("7235829")).PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2010/06/22 16:03
S112	275	capacit\$3 near resist\$3) near gate	US-PGPUB; USPAT	OR	ON	2010/06/23 12:28
S113	113	S112 same (MOS fet transistor)	US-PGPUB; USPAT	OR	ON	2010/06/23 12:30
S114	6	S113 same esd	US-PGPUB; USPAT	OR	ON	2010/06/23 12:30
S115	2008	(capacit\$3 near resist\$3) near series	US-PGPUB; USPAT	OR	ON	2010/06/23 12:31
S116	1	S115 near gate near (diode transistor MOS fet)	US-PGPUB; USPAT	OR	ON	2010/06/23 12:32
S117	60	S115 near (diode transistor MOS fet)	US-PGPUB; USPAT	OR	ON	2010/06/23 12:33
S118	8	esd same (detect\$3 trigger \$3 delay) same (capacit\$3 near resist\$3 near series)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/06/23 12:38
S119	262	esd same (detect\$3 trigger \$3 delay) same (capacit\$3 near resist\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/06/23 12:41
S120	129	S119 same (MOS FET transistor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/06/23 12:41
S121	58	S119 same inverter	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/06/23 13:00

S122	56	S119 same delay	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/06/23 13:45
S123	17	"4855620" "5086365" "5237395" "5255146" "5287241" "5311391" "5440162" "5610791" "5625280" "5631793" "6008970").PN. OR ("6912109").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2010/06/23 13:51
S124	328	(Ming-Dou near Ker).IN.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/06/23 13:54
S125	11	((diode gate ground) near connected near (MOS transistor fet)) and capacit \$3 and resist\$3 and series and esd).CLM.	US-PGPUB; USPAT	OR	ON	2010/06/23 14:27
S126	64	((diode gate ground) near connected) and (MOS transistor fet) and capacit \$3 and resist\$3 and series and esd).CLM.	US-PGPUB; USPAT	OR	ON	2010/06/23 14:30
S127	53	S126 not S125	US-PGPUB; USPAT	OR	ON	2010/06/23 14:31
S128	3	(esd and (detect\$3 trigger \$3 delay) and(capacit\$3 near resist\$3 near series)). CLM.	US-PGPUB; USPAT	OR	ON	2010/06/23 14:35
S133	99	inverter same capacit\$3 same resist\$3 same series same esd	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/06/24 10:40

S134	34	("4724471" "5117129" "5255146" "5287241" "5436183" "5508649" "5515225" "5530612" "5559659" "5654862" "5740000" "5744842" "5745323" "5907464" "5946177" "5986861" "5991135" "6014298" "6072682" "6091593" "6385028" "6404269" "6510033").PN. OR ("6801416").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2010/06/24 10:43
S135	13	("20040107328" "20040109270" "20050057866" "5239440" "5946177" "6008970" "6369994" "6388850" "6522511" "6728086" "6801416" "6958896" "7164565").PN. OR ("7245468").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2010/06/24 12:42
S136	2174	(361/111).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/06/24 14:21
S137	109	S136 and inverter and capacit\$3 and resist\$3 and series and esd	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/06/24 14:22
S138	109	S137 and (MOS fet transistor)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2010/06/24 14:22
S139	33	("5237395" "5416661" "5465190" "5530612" "5559659" "5568346" "5610790" "5610791" "5617283" "5625280" "5631793" "5642252" "5644459" "5654862" "5679971" "5686751" "5706156" "5721656" "5729419" "5744842" "5745323" "5874834" "5907464").PN. OR ("6292343").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2010/06/24 14:25

S140	103	(361/91.3).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/06/24 14:34
S141	8	S140 and esd	US-PGPUB; USPAT	OR	ON	2010/06/24 14:34
S142	772	(361/83,94).CCLS.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2010/06/24 14:40
S143	1	S142 and esd and inverter	US-PGPUB; USPAT	OR	ON	2010/06/24 14:40
S144	98	S142 and inverter	US-PGPUB; USPAT	OR	ON	2010/06/24 14:40
S145	81	S142 and inverter and (MOS fet transistor)	US-PGPUB; USPAT	OR	ON	2010/06/24 14:41

EAST Search History (Interference)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S129	2	(esd and (detect\$3 trigger\$3 delay) and (capacit\$3 near resist \$3 near series)).CLM.	USPAT; UPAD	OR	ON	2010/06/23 14:36
S130	53	(esd and (detect\$3 trigger\$3 delay) and capacit\$3 and resist\$3 and series).CLM.	USPAT; UPAD	OR	ON	2010/06/23 14:37
S131	1897	esd and (detect\$3 trigger\$3 delay) and capacit\$3 and resist\$3 and series	USPAT; UPAD	OR	ON	2010/06/23 15:09
S132	40	esd and (detect\$3 trigger\$3 delay) and (capacit\$3 near resist \$3 near series)	USPAT; UPAD	OR	ON	2010/06/23 15:09

6/ 24/ 2010 2:59:09 PM

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